

## Rabaey Digital Integrated Circuits Chapter 12

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### **Jan M. Rabaey | EECS at UC Berkeley**

Class Schedule. This may be updated on a regular basis - make sure to check often! Lecture notes will be posted at least 1 hour before the lecture.

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Discussion Sections. Discussion sections (aka recitations) are also provided on video tape. Some students find these tapes useful as an additional review of course material.

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Step 1 of 9 The source resistance is  $R_s$ , The load resistance is  $R_L$ , The input voltage is  $V_i$ , Comment(0) Step 2 of 9 The following is the transmission line: Comment(0) Step 3 of 9 Now, so reflection coefficient at load is  $\Gamma_L$ . Since  $R_L \gg Z_0$ , large portion of  $V_i$  will be injected which can be determined from voltage divider formed by  $Z_0$  and  $R_L$ .

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